

Induction Motor Drive Based on Vector Control Technique and Active Power Factor Correction for The Three-Phase Rectifier Systems Using The Three-Level Converters

Abstract. This paper presents a comparison of the modulation methods of space vector power control in combination with a Vienna three-phase rectifier using two control methods: 1. Power Control 2. Current Control There are three modulations tested: 1. Pulse width modulation using a phase-shifted triangle signal 2. Modulating pulse width with a shifted triangle signal 3. Pulse width modulation by vector and d-q axis control is also used to stabilize the distortion of the input current and DC output voltage. The pulse width modulation method with a phase-shifted triangle signal was discovered to have a total input current distortion of as low as 2.28 percent, and the DC output can be stabilized at 800 VDC with a rated power of 10 kW. All results were obtained from the programs MATLAB and the test suites DSP. The simulation confirmed the effectiveness of control and modulation for the design and selection of suitable methods for the Vienna three-phase rectifiers.

Streszczenie. W artykule przedstawiono porównanie metod modulacji wektora przestrzennego sterowania mocą w połączeniu z wieńskim prostownikiem trójfazowym przy użyciu dwóch metod sterowania: 1. Sterowanie mocą 2. Sterowanie prądem Testowane są trzy modulacje: 1. Modulacja szerokości impulsu z wykorzystaniem fazy - sygnał z przesuniętym trójkątem 2. Modulacja szerokości impulsu z przesuniętym sygnałem trójkąta 3. Modulacja szerokości impulsu za pomocą sterowania wektorowego i osi d-q jest również wykorzystywana do stabilizacji zniekształceń prądu wejściowego i napięcia wyjściowego DC. Odkryto, że metoda modulacji szerokości impulsu z przesuniętym w fazie sygnałem trójkątnym powoduje całkowite zniekształcenie prądu wejściowego na poziomie zaledwie 2,28 procent, a napięcie wyjściowe prądu stałego można ustabilizować na poziomie 800 VDC przy mocy znamionowej 10 kW. Wszystkie wyniki uzyskano z programów MATLAB oraz zestawów testowych DSP. Symulacja potwierdziła skuteczność sterowania i modulacji przy projektowaniu i doborze odpowiednich metod dla wieńskich prostowników trójfazowych. (Napęd silnika indukcyjnego oparty na technice sterowania wektorowego i korekcji współczynnika mocy czynnej dla trójfazowych układów prostowniczych z wykorzystaniem przekształtników trójpoziomowych)

Keywords: Pulse width modulation using a phase-shifted triangle signal, Current control, d-q control, Pulse

Słowa kluczowe: napęd, silnik indukcyjny, przekształtnik trójpoziomowy

Introduction

The Vienna rectifier in Figure 1 is a three-phase, three-level voltage rectifier with high reliability, high efficiency, and minimal occurrences of total harmonic distortion, which is an active filter circuit, smaller size, higher efficiency, and higher power factor compared to the passive filter method.

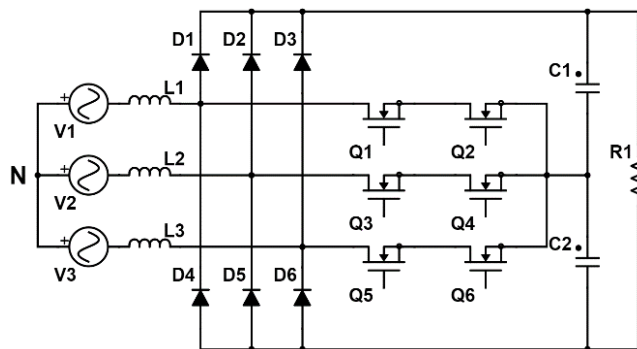


Fig.1. Vienna three-phase rectifier

The Vienna three-phase rectifier uses control by the d-q method and the modulation method. The control of switching devices in Vienna rectifiers is the space-vector pulse width modulation, which is a three-level vector space consisting of 27 states in switching and 19 voltage vectors as shown in Figure 2.

Due to a large number of switching states and voltage vectors, complex calculations are possible. Therefore, this paper will introduce how to use three levels of vector space as a two-level vector computation method, which will make it easier to calculate active vector space. Together with the

Vienna three-phase rectifier, the circuit performance remains the same.

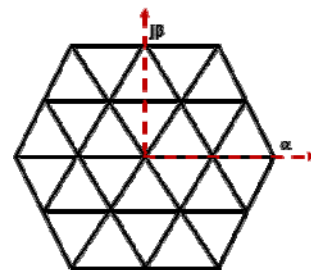


Fig.2. three-level vector space

The method for controlling a three-phase Vienna rectifier

The control method on the d-q axis is shown in Figure 3. The input voltage (V_a , V_b , V_c) and input current (I_a , I_b , I_c) are converted from a three-phase system into a rotating reference frame using the Clark transform principle as shown in Equations 1, 2, and 3, and the Park transform as shown in Equations 4-6.

$$(1) \quad i_\alpha = \frac{2}{3} i_a - \frac{1}{3} (i_b + i_c)$$

$$(2) \quad i_\beta = \frac{2}{\sqrt{3}} (i_b - i_c)$$

$$(3) \quad i_0 = \frac{1}{3} (i_a + i_b + i_c)$$

$$(4) \quad i_d = i_\alpha \cdot \cos(\theta) + i_\beta \cdot \sin(\theta)$$

(5)

$$i_q = -i_\alpha \cdot \sin(\theta) + i_\beta \cdot \cos(\theta)$$

The i_d is the current in the D axis, which is like the real power, and i_q is the current in the Q axis, which is the reactive power. The i_d is then derived from the PI controller used to control the DC voltage. The output and the i_q are set to zero in order to control only the power factor close to one. The currents i_d and i_q are controlled by a PI regulator used to control the current as in Equations (6) and (7).

$$(6) \quad u_d = -\left(K_p + \frac{K_i}{s}\right)(i_{d\text{ref}} - i_d) + v_d + \omega L i_q$$

$$(7) \quad u_q = -\left(K_p + \frac{K_i}{s}\right)(i_{q\text{ref}} - i_q) + v_q - \omega L i_d$$

When converting the voltage in the form of a rotary reference frame back into a three-phase power system for use in modulation by the reversing principle of Park is shown in Equation (8), and Clark's reversal shown in Equation 10-12.

$$(8) \quad i_\alpha = i_d \cdot \cos(\theta) - i_q \cdot \sin(\theta)$$

$$(9) \quad i_\beta = i_d \cdot \sin(\theta) + i_q \cdot \cos(\theta)$$

$$(10) \quad i_a = i_\alpha$$

$$(11) \quad i_b = -\frac{1}{2} \cdot i_\alpha + \frac{\sqrt{3}}{2} \cdot i_\beta$$

$$(12) \quad i_c = -\frac{1}{2} \cdot i_\alpha - \frac{\sqrt{3}}{2} \cdot i_\beta$$

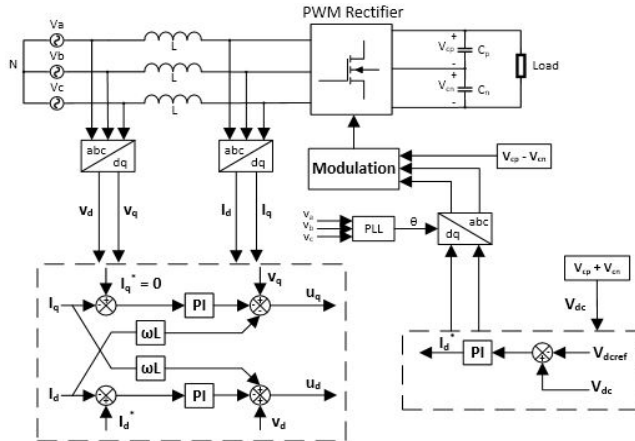


Fig.3. Control diagram on the d-q axis.

The three-level space vector modulation method is a two-level space vector

1. Conversion to the origin of the three-level space vector

As shown in Figure 4(a), this method converts the origin of the three-level space vector, which consists of 19 voltage vectors, as follows: The zero vector (v_0) has a size of zero. A small vector ($v_1, v_2, v_3, v_4, v_5, v_6$) has a size of $V_{dc}/3$, a medium vector ($v_8, v_{10}, v_{12}, v_{14}, v_{16}, v_{18}$) has a size of $3V_{dc}/3$, and large vectors ($v_7, v_9, v_{11}, v_{13}, v_{15}, v_{17}$) have the same size as $2V_{dc}/3$ points to a small vector $v_1, v_2, v_3, v_4, v_5, v_6$. This method gives the vector space a shape similar to the two-level vector space shown in Figure 4(b), and the

reference voltage vector can be written as shown in Equations 13 and 14.

$$(13) \quad T_x V_x + T_y V_y + T_z V_z = T_s V_{\text{REF_MAP}}$$

$$(14) \quad T_x + T_y + T_z = T_s$$

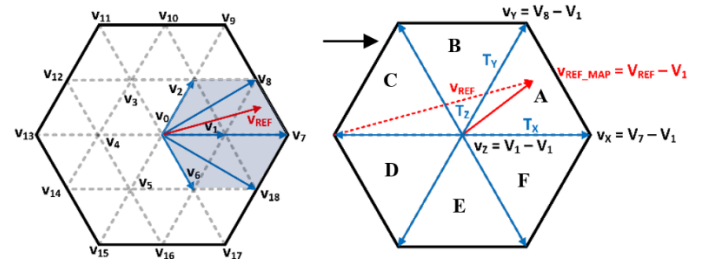


Fig.4. The reference voltage vector in a three-level vector space, (b) The reference voltage vector after moving the starting point in a two-level vector space

A two-level vector space sector can be divided into six three-level vector space sectors as shown in Figure 5 and will get the size of v_z in each sector according to Table 1.

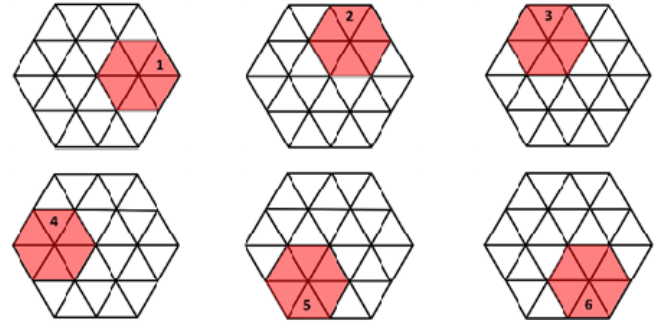


Fig.5. Divides the three-level vector space into 6 sectors.

Table 1. The zero vector position of each sector and its magnitude.

Sectors.	The zero vector position	The size of V_z
1	V_1	$V_{dc}/3$
2	V_2	$V_{dc}/6$
3	V_3	$-V_{dc}/6$
4	V_4	$-V_{dc}/3$
5	V_5	$-V_{dc}/6$
6	V_6	$V_{dc}/6$

2. Calculation of the duty cycle time T_x, T_y, T_z

The duty cycle time equation [13–15] can be used in the same way as for a two-level vector space as in Equation 15.

$$(15) \quad \begin{bmatrix} V_x \\ V_y \end{bmatrix} \frac{T_s}{2} = \frac{V_{dc}}{3} \begin{bmatrix} \cos\left(\frac{(n-1)\pi}{3}\right) & \cos\left(\frac{n\pi}{3}\right) \\ \sin\left(\frac{(n-1)\pi}{3}\right) & \sin\left(\frac{n\pi}{3}\right) \end{bmatrix} \begin{bmatrix} T_x \\ T_y \end{bmatrix}$$

When n denotes the 1-6 subsectors of the two-level vector space and T_s denotes the random time. Therefore, when calculating the duty cycle time, it will be as shown in Equations 16-18. The position vectors and duty cycle times of each subsector in sector 1 can be summarized in Table 2.

$$(16) \quad X = 2\sqrt{3}V_y \times \frac{T_s}{V_{dc}}$$

$$(17) \quad Y = 3V_x + \sqrt{3}V_y \times \frac{T_s}{V_{dc}}$$

$$(18) \quad Z = 3V_x - \sqrt{3}V_y \times \frac{T_s}{V_{dc}}$$

Table 2. Summarize the position value of the vector and the duty cycle time of sector 1.

sub sector	The position value of the vector			the duty cycle time		
	V_x	V_y	V_z	T_x	T_y	T_z
A	$V_7 - V_1$	$V_8 - V_1$	$V_1 - V_1$	Z	X	$T_s - X - Z$
B	$V_8 - V_1$	$V_2 - V_1$	$V_1 - V_1$	Y	-Z	$T_s - Y + Z$
C	$V_2 - V_1$	$V_0 - V_1$	$V_1 - V_1$	X	-Y	$T_s - X + Y$
D	$V_0 - V_1$	$V_6 - V_1$	$V_1 - V_1$	-Z	-X	$T_s + Z + X$
E	$V_6 - V_1$	$V_{18} - V_1$	$V_1 - V_1$	-Y	Z	$T_s + Y - Z$
F	$V_{18} - V_1$	$V_7 - V_1$	$V_1 - V_1$	-X	Y	$T_s + X - Y$

3. Determination of the switching sequence

A vector symmetric method is used to determine the switching sequence in a two-level vector space. For example, the switching sequence of sub-sector 1 in sector 1 is shown in Figure 6, and the switching sequence is as shown in equation 19.

$$(19) \quad V_{1+} - V_8 - V_7 - V_{1-} - V_{1-} - V_7 - V_8 - V_{1+}$$

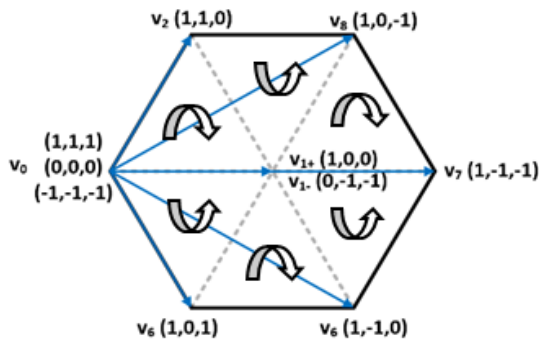


Fig.6. Switching sequence for sector 1

The modulation condition is determined by the direction of the input current. For example, the current in phase A of sector 1 is positive and thus modulated by the positive triangle signal. The currents in phase B and phase C are negative, so they are modulated by the negative triangle signal as shown in Figure 7.

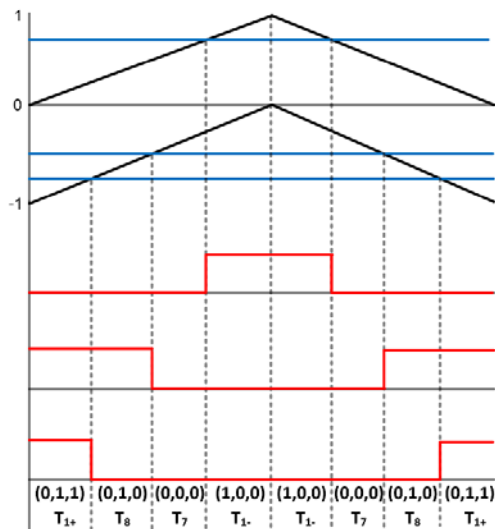


Fig.7. S Modulation in sub-sector 1 of sector 1

Results

In this test, the DSP is used to determine the operation resulting in a sinusoidal input current, control the output DC voltage, and measure the total distortion of the input current with the following in table 3 parameters:

Table 3. Parameters in this test

Parameters	Value
Input Voltage	380 V _{AC}
Output Voltage	800 V _{DC}
Output Power	10 kW
Switching Frequency	20 kHz
Inductor	3 mH
Capacitor	220 μF
Sample Time	1e ⁻⁶ s

In Figure 8, the input current is controlled by modulation, resulting in a sinusoidal nature and low input current distortion. From Figure 9, the input voltage (Va) and the input current (Ia) in the steady state do not overlap, causing the power factor to approach one. From Figure 10, the output DC voltage is regulated at a constant of 800Vdc. When the line-to-line voltage is represented by three voltages, as shown in Figure 11, and the distortion of the input current is measured, as shown in Figure 12, the distortion is 2.28 percent.

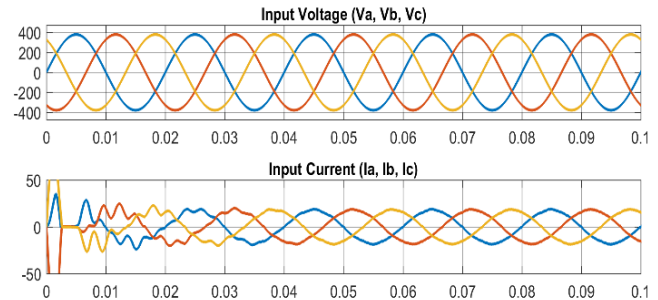


Fig.8. Input voltage (Va, Vb, Vc) and input current (Ia, Ib, Ic)

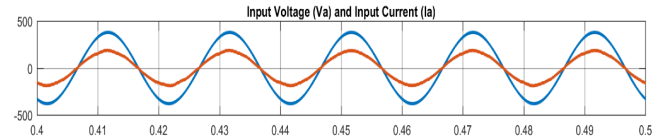


Fig.9. Input voltage (Va, Vb, Vc) and input current (Ia, Ib, Ic)

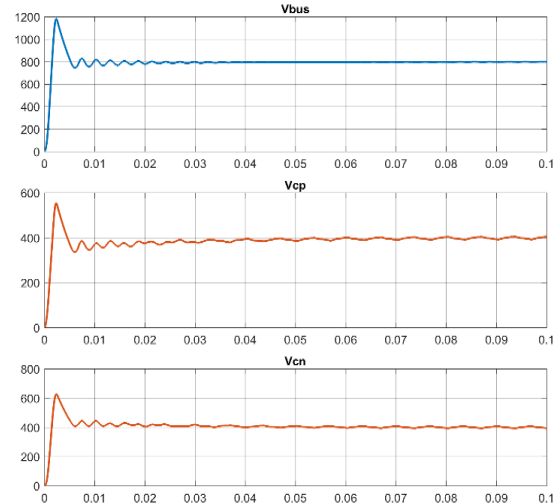


Fig.10. Input voltage (Va, Vb, Vc) and input current (Ia, Ib, Ic)

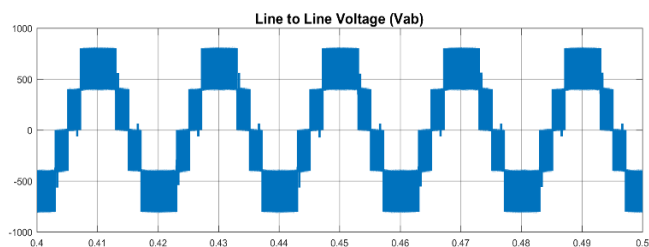


Fig.11. line-to-line voltage (Vab)

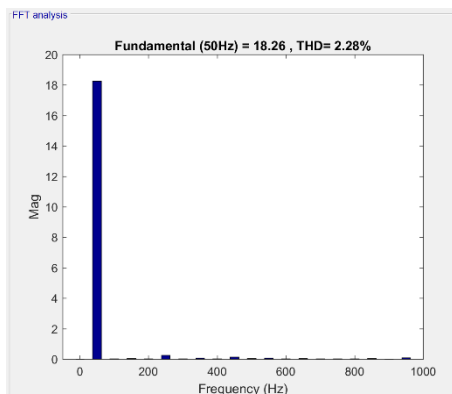


Fig.12. Distortion of input current

Conclusions

This paper presents a space vector pulse width modulation method of a three-level converter circuit in a Vienna rectifier using the three-level space vector from a two-level vector space. Three-level vector spaces are typically complex computations, whereby they simplify computations by using duty-cycle-time computations, and the switching sequence is the same as the two-level vector method, which can also reduce the total input current distortion to as low as 2.28 percent. It is also able to maintain a constant DC output voltage of 800 Vdc.

Authors: Gontapon Promnigon, Ph.D.(Candidate), Faculty of Engineering Mahasarakham University, Tambon Khamriang, Kantharawichai District, Maha Sarakham 44150 Thailand.

Jagrathon OBMA, Ph.D., Faculty of Engineering, Rajamangala University of Technology ISAN Khonkaen Campus, Khonkaen 40000, Thailand.

Krittanon Prathepha Ph.D., Faculty of Engineering, Mahasarakham University, Tambon Khamriang, Kantharawichai District, Maha Sarakham 44150 Thailand.

Narongkorn UTHATHIP Ph.D., Faculty of Engineering, Mahasarakham University, Tambon Khamriang, Kantharawichai District, Maha Sarakham 44150 Thailand.

Nuttapon CHAIDUANGSRI Ph.D., Faculty of Engineering, Mahasarakham University, Tambon Khamriang, Kantharawichai District, Maha Sarakham 44150 Thailand.

Assoc.Prof.Worawat Sa-ngiamvibool, Ph.D., Faculty of Engineering, Mahasarakham University, Tambon Khamriang, Kantharawichai District, Maha Sarakham 44150 Thailand.
e-mail : wor.nui@gmail.com (Corresponding Author)

REFERENCES

- [1] Hari Charan Nannam, Atanu Banerjee, IEEE, "A Detailed Modeling and Comparative Analysis of Hysteresis Current Controlled Vienna Rectifier and Space Vector Pulse Width Modulated Vienna Rectifier in Mitigating the Harmonic Distortion on the Input Mains", 2018 IEEE International Conference on Industrial Engineering and Engineering Management (IEEM) 2018
- [2] June-Seok Lee, Kyo-Beum Lee, IEEE, "Carrier-Based Discontinuous PWM Method for Vienna Rectifiers", IEEE Transactions on Power Electronics 2015
- [3] Li Zhang, Rui Zhao, Ping Ju, Chuanhao Ji, Yuhang Zou, Yan Ming, Yan Xing, "A Modified DPWM With Neutral Point Voltage Balance Capability for Three-Phase Vienna Rectifiers", IEEE, IEEE Transactions on Power Electronics 2021
- [4] Boning Wu, Zhiqiang Gao, Xuesong Zhou, Youjie Ma, Chenglong Wang, "Research and Simulation of DC Microgrid Three-Phase AC-DC Converter Control Strategy Based on Double Loop", IEEE, IEEE Access 2020
- [5] Mahdi Ashabani, Yasser A.-R.I. Mojtaba Mirsalim, Mohammad Aghashabani, "Multivariable Droop Control of Synchronous Current Converters in Weak Grids/Microgrids With Decoupled dq-Axes Currents", IEEE, IEEE Transactions on Smart Grid 2015
- [6] Bo Shuang, Zi-Qiang Zhu, "A Novel Method for Estimating the High Frequency Incremental DQ-Axis and Cross-Coupling Inductances in Interior Permanent Magnet Synchronous Machines", IEEE, IEEE Transactions on Industry Applications 2021
- [7] Ruirui Chen, Jiahao Niu, Handong Gui, "Modeling, Analysis, and Reduction of Harmonics in Paralleled and Interleaved Three-Level Neutral Point Clamped Inverters With Space Vector Modulation", IEEE, IEEE Transactions on Power Electronics 2020
- [8] Vinoth Jayakumar, Bharatiraja Chokkalingam, Josiah Lange Munda, "Performance Analysis of Multi-Carrier PWM and Space Vector Modulation Techniques for Five-Phase Three-Level Neutral Point Clamped Inverter", IEEE, IEEE Access 2022
- [9] A. Karthik, Umanand Loganathan, "A Simple Dual Three-Level Inverter Topology With Improved Fault Tolerance", IEEE, IEEE Journal of Emerging and Selected Topics in Power Electronics 2021
- [10] Vinod B.R., Baiju M.R., "Space Vector based DTC scheme for a 3-Level inverter realized by cascading two 2-Level inverters", IEEE, 2016 IEEE 1st International Conference on Power Electronics, Intelligent Control and Energy Systems (ICPEICES) 2016
- [11] M.T. Shah, P.N. Tekwani, "Generalized current control scheme for unity power factor two-level and three-level bi-directional front-end power converters: An approach for multi-level front-end converters", IEEE, 8th IET International Conference on Power Electronics, Machines and Drives (PEMD 2016)
- [12] J. Kalaiselvi, K. Rama Chandra Sekhar, S. Srinivas, "Common mode voltage elimination PWMs for a dual two-level VSI with single inverter switching", IEEE, 2012 IEEE International Symposium on Industrial Electronics 2012
- [13] Ricardo Carrano, Diego Passos, Luiz Magalhaes, "An Exact Model of the Neighbor Discovery Time for Schedule-Based Asynchronous Duty Cycling", IEEE, IEEE Wireless Communications Letters 2013
- [14] Kuo-Kai Shyu, Yun-Jen Chiu, Po-Lei Lee, "Adaptive SSVEP-Based BCI System With Frequency and Pulse Duty-Cycle Stimuli Tuning Design", IEEE, IEEE Transactions on Neural Systems and Rehabilitation Engineering 2013
- [15] Xing Liu, Dan Wang, Zhouhua Peng, "A Computationally Efficient Fcs-MPC Method without Weighting Factors for NNPCs With Optimal Duty Cycle", IEEE, IEEE/ASME Transactions on Mechatronics 2018
- [16] Pengyu Zhang, Liang Shen, Tailai Wen, "Vector Phase Symmetry for Stable Hyperbola Detection in Ground-Penetrating Radar Images", IEEE, IEEE Transaction on Geoscience and Remote Sensing 2022
- [17] Benjamin M. Zaidel, Ralf R. MULLER, Aris L. Moustakas, "Vector Precoding for Gaussian MIMO Broadcast Channels: Impact of Replica Symmetry Breaking", IEEE, IEEE Transactions on Information Theory 2012
- [18] Federica Poli, Enrico Coscelli, Annamaria Cucinotta, "Single-Mode Propagation in Yb-Doped Large Mode Area Fibers With Reduced Cladding Symmetry", IEEE, IEEE Photonics Technology Letters 2014
- [19] Tomasz Balcowicz, (2017) "Three-Phase Rectifier Dedicated to DC Traction Substation", Przegląd Elektrotechniczny 1(9):43-47
- [20] Sarfaro DOVUDOV, Mikhail DUNAEV, Anastasia ZHIRAVETSKA, Oleksandr PLAKHTII, Rustam Yunusov, and Murodbek SAFARALIEV, (2022) "Analysis and comparison of energy indices of dc-dc pulse converters", Przegląd Elektrotechniczny, 1(6):101-105.