Enhancement Performance of DSTATCOM depending on PV System Supply control

Abstract. This article examines the impacts of a transient analysis of a compensator of static synchronous on a system of distribution power (DSTATCOM) on power quality when a PV system DC input source is utilised to supply the system. Due to the nature of these sources' operation, including renewable energy on the input power supply has a variety of ramifications for DSTATCOM's operation. In order to understand the variance in temporal response and its impact on the compensatory process, the dynamic response of the distribution system is investigated under the influence of both battery and PV system sources (PV-Battery). Renewable energy is connected across the dc-link capacitor channel to characterize their impact on the power system's dynamic performance. In this study, three scenarios were investigated: first, photovoltaic (PV) cells are connected solely via the dc-link, second, battery storage is connected solely, and third, a PV system structure (PV cells + battery storage) is explored. DSTATCOM is used to compensate for the reduction in reactive and active power that happens during balanced and unbalanced operation of non-linear loads. The presence of a renewable source enhances the system's power quality by reducing the current source's harmonic components. The Star / Delta transformer is also used to split the three-phase legs of the DSTATCOM VSC, providing a channel for the fundamental zero sequence and a balanced three-phase current. STATCOM control circuit based on synchronous reference frames (SRF).

Introduction

Renewable sources of energy (RSE) have emerged as the preferred answer to power system challenges. As a result, current efforts place a premium on improving the performance of various energy source kinds. The process of getting, utilising, and consuming this energy correctly results in high power quality systems. It is critical to minimise losses in order to get a timely reaction of power compensation from a renewable energy source. [1, 2]. The photovoltaic generated electricity acquired from solar cell sources is variable and influenced by weather circumstances such as variations in solar radiation as well as the effect of high temperatures, etc[3]. A MPPT algorithm is introduced to obtain a maximum power generated by solar panels [4]. Maximum power can be created using a variety of methods that also improve overall system efficiency. The Observe and Perturb (O &P) approach, method of perturb and observe, the open-circuit voltage method, and the constant voltage method are among the present work's strategies [5, 6]. The P&O approach is connected to a DC-DC boost converter circuit to generate the switch ON/OFF pulse of an Insulated Gate Bipolar Transistor (IGBT). To obtain generated extreme power, the circuit of boost is connected across the PV cells[7]. The solar cells' generated power is delivered into the STATCOM input Dc voltage link via the boost Dc-Dc circuit. Extra power generated by PV cells is stored in an energy storage unit (battery) [8]. The battery-stored energy is used to power the compensator's input DC voltage connection during night intervals or in severe weather conditions where solar radiation is very low or absent. A buck-boost circuit is used to regulate the battery output voltage and enhance the charge/discharge cycle to achieve high charge and discharge accuracy. To provide consistent coordination performance between PV and battery operation, a logic circuit is used [9-13].

To regulate the compensation of DSTATCOM, the synchronous reference frame theory with unit vector approach is considered [14, 15]. Three-phase four-wire distribution systems have a number of power quality concerns due to harmonics caused by non-linear loads, unbalance in the neutral load, and other reasons [14]. IGBT devices utilised as switching transistors in the VSC provide a quick reaction for the inverter output voltage, which improves DSTATCOM correction performance. [16]. A number of compensating devices are used to improve power quality, voltage profile, power factor, and total harmonics (THD) distortion factor. The Static Var Compensator (SVC) is one of them, and it is regarded as a classic kind because it relies on a big capacitor and inductance for correction [17]. The introduction of more advanced SVC gear has stemmed from the quick advancement of power electrical gadgets. It is distinguished by its tiny size, quick compensating response, and controllability. These devices include the static synchronous compensator (STATCOM), static synchronous series compensator (SSSCA), and unified power flow controller (UPFC) .The UPFC is the finest compensator among all flexible a.c transmission system (FACTS) devices. The composite control circuits and high production costs are
disadvantages [18-19]. In the current work, a STATCOM device was used to improve power quality because its control circuit is simpler than that of a UPFC, it is small in size, and it has a high compensating capability [20]. The unit vector approach of the SRF is well suited for manipulating the DSTATCOM control circuit [22, 23].

The study’s contribution is summed up in a high coordination of PV system supply system. They collaborate as a PV system to supply DSTATCOM’s needed DC link voltage.

This study examines and assesses the effects of DSTATCOM time-response of power compensation when different types of supply sources are linked across the DC link input voltage. Four approaches are considered. First, the STATCOM with DC input voltage alone is utilized; second, the PV panel is supplied with the DC voltage channel; third, the battery storage unit is regarded to supply the DC voltage link; and finally, the STATCOM DC input voltage is supplied by a PV system PV-battery storage unit. An analysis of the proposed compensatory power sources reveals that the DSTATCOM input voltage is determined by which sources attain steady state first.

Methodology of PV system with DSTATCOM

A multi-sources compensation system (PV-battery storage unit) is an effective method for improving dynamic response and compensating for power quality reductions (low power factor, high THD, injected harmonics current components, low stability, low response time, and so on) caused by non-linear loads and system distribution disturbances. During the compensation stage, a good coordination design is required. The PV system sources' performance is analyzed and evaluated to indicate their impact on the distribution system. As a compensation device, a DSTATCOM voltage source inverter (VSI) with IGBT solid-state switches is used. In this paper, the compensator’s control methodology is based on synchronous reference frame with unit vector technique.

Constriction of DSTATCOM with PV system

Fig1 depicts the DSTATCOM distribution power system design with control method (SRF and unit vector) based on PV system input DC voltage sources. The photovoltaic boost converter and battery back-boost converter power the multi-source power supply. The grid is linked to the DSTATCOM compensating system with a star/delta transformer at a common coupling point [22]. The non-linear load that is consist of three-phase inverter is connected to the system. This system supplied by DC voltage source Vdc connected across the capacitor where its value can be calculated using Equ. 1.

\[
V_{dc_{	ext{act}}} = \frac{2\pi V_{L}}{\sqrt{3} m} \tag{1}
\]

In addition, the DC-link capacitance \( C_{d} \) that is connected across the input of DSTATCOM is formulated as shown in Eq. Two [23]:

\[
0.5 C_{d} \left( V_{dc_{ref}}^{2} - (V_{dc_{actual}}^{2}) \right) = 3V_{ph}(OLI_{ph})t \tag{2}
\]

The phase voltage of the considered power system is computed based on Eq. 3 as follows:

\[
V_{ph} = \frac{V_{\text{line}}}{\sqrt{3}} \tag{3}
\]

where: \( m \) is the modulation index that is normally suppose equal to 1, \( V_{dc_{ref}} \) is the DC reference input voltage which is assuming about 680 V, \( V_{dc_{act}} \) is the actual input voltage of STATCOM DC which is equal to 678.69 V. \( V_{L} \) is the line-line power system voltage where it is assumed equal to 415 V, \( C_{d} \) is the DC connection input channel capacitor and equal 2900 \( \mu F \), \( OLI_{ph} \) over load factor where it is assumed equal to 1.2, \( I_{ph} \) is the grid phase current and it is assumed equal 57.14 A, \( V_{ph} \) is the phase voltage and it is equal 240 V, the recovery time of DC connection voltage equal 355 \( \mu s \).

![Fig 2. PV system equivalent circuit](image)

Fig 2 [21] depicts the comparable circuit of a PV renewable energy system. A dc-dc boost converter connects the PV module to the common input voltage dc bus. Sanyo solar modules make up the PV system. These solar modules are wired in series to form groups, then in parallel to form the PV array. The solar module in Fig 2 is a current source with a parallel diode. PV farms use maximum power point tracking (MPPT) technology to ensure maximum solar energy generation. The DC-DC boost converter’s triggering gate is used as a controller to attain the highest output PV array voltage. Different MPPT procedures for solar PVs have been investigated; the current study uses a Perturb & Observe (P&O) methodology. The output PV voltage is always regulated to attain a maximum voltage for varying solar irradiation variations due to the influence of MPPT. As a result, the generated PV is at its peak under sun radiation. Table 1 [24] lists the V-I specs.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power max</td>
<td>240 W</td>
</tr>
<tr>
<td>Open circuit voltage</td>
<td>37 V</td>
</tr>
<tr>
<td>Current of short circuit</td>
<td>8.3 A</td>
</tr>
<tr>
<td>Maximum power voltage (V.pm)</td>
<td>30 V</td>
</tr>
<tr>
<td>Maximum power current (I.pm)</td>
<td>6.65 A</td>
</tr>
<tr>
<td>Temperature coefficient of short circuit current</td>
<td>46 mA/°C</td>
</tr>
<tr>
<td>Open circuit voltage coefficient of Temperature</td>
<td>- 0.109V/°C</td>
</tr>
</tbody>
</table>

![Table 1. The datum of solar panel model at 1000 W/m² and temperature of 25°C.](image)
The following relationships summarise the fluctuation in solar irradiation [21].

\[ I = I_{pv} - I_0 \left| \exp \frac{V_{oc} q}{n_{R_{th} R_{SH}}} - 1 \right| - \frac{V_{oc}}{R_{SH}} \]  

The PV module current is calculated using the following equation, which is dependent on two factors: the linear variation of solar irradiation and the temperature.

\[ I_{pv} = \left( I_{pv,s} + K_t \Delta_t \right) \frac{g_s}{g_s} \]  

The \( I_{pv,s} \) value is evaluated using equation (7) as shown in Fig. 2.

\[ I_{pv,s} = \frac{R_{SH} + R_s}{R_{SH}} I_{EH} \]  

Where: \( I_{pv} \) and \( I_s \) are the PV current and initial current of one cell respectively, where the number of cells that connected (NC), \( K_t \) is the boltzmann constant and it is to \((1.38065 \times 10^{-23}) \text{J/K})\), \( t \) is temperature of one cell, \( q \) is the PV electronical and equal \((1.60217 \times 10^{-19}) \text{C})\), \( R_{SH} \) is the electrical circuit’s shunt resistance, \( R_s \) is the electrical circuit’s series resistance, \( I_0 \) is the PV cell factor where its difference \( b, \) and \( g, g_s \) are the panel surface irradiation and normal irradiation, respectively [19].

DC link voltage control Approaches

There are two types of circuits for control of a DC link's voltage are as follows:

**Boost converter circuit of Photovoltaic**

The output power of PV cells had to be adjusted constantly when considering the DC-DC boost converter circuit. Fig3 illustrates this [25], this circuit is convenient for controlling the input DC Link supply voltage for VSI in response to variations in the applied load.

**Maximum power point tracking (MPPT) Technique**

As previously indicated, the Perturb & Observe Technique (P&O) is being used in current research to achieve MPPT of output-generated power [23]. P&O is used to achieve the control IGBT train pulses interval (D) [24].

\[ \frac{dI_{pf}}{dV_{pf}} = -\frac{I_{pf}}{V_{pf}} \]  

\[ \frac{dV_{pf}}{dI_{pf}} \geq \frac{I_{pf}}{V_{pf}} \]  

Where: \( V_{pf}, I_{pf} \) represent the voltage and current of the PV respectively, \( dV_{pf}, dI_{pf} \) are the average change values of PV voltage and current respectively. When the derivative in equation (8) equals zero, the MPP scenario is obtained. If the power is less than MPP, the calculation says When the derivative in equation (8) equals zero, the MPP scenario is obtained. If the power is less than MPP, the \( V_{pf} \) value must be increased to attain MPP, according to the equation (9). If the PV produced power is more than the MPP, the \( V_{pf} \) must be reduced to reach the MPP situation, according to equation (10).

**DC to DC buck-boost**

Since of its deep charging duration and substantial available capacity, lead acid batteries are used in conjunction with PV arrays because they are more common and compatible with many types of inverters [25, 26]. When the generated power from the solar panels exceeds the demand load, the buck boost converter absorbs the excess solar electricity. In the absence of a solar panel or when generation is small, the boost circuit kicks in to compensate for the power shortfall, as shown in Fig(2).

The following equations [11] can be used to explain the charge/discharge charging lead acid battery cycle:

\[ V_{Rsh} = V - R_{sh} - b \frac{q}{q-\alpha(t)} (i(t) + t') \left( \frac{q}{q-\alpha(t)} \right) + e p(t) \]  

\[ V_{Rsh} = V - R_{sh} - b \frac{q}{q-\alpha(t)} (i(t) + t') \left( \frac{q}{q-\alpha(t)} \right) + e p(t) \]  

Where \( V_{Rsh} \) represent charge/discharge voltage of the battery and battery constant voltage respectively \( V \), \( k \) is the constant of polarisation \( \text{V/Ah}) \), \( q \) is the capacity of battery \( \text{Ah}) \), \( (i(t)) \) is the actual power of battery charging \( \text{A/h}) \) and calculate according to equation (16). \( i_b \) is the battery current \( \text{A}) \), \( (t') \) is the current of filter \( A) \) and \( (t') \) is the time of filter \( h) \).

**Control algorithm of DSTATCOM**

To Various control structure methods are utilised to replicate the DSTATCOM reference source currents (isaref, isref, and iscref) control circuits, synchronous reference frame (SRF) theorems, decoupled current control, and instantaneous reactive power theorems (p-q theory). A SRF was used in the current work [14, 15, 29, 30, 31] in the present paper.

The voltages of DSTATCOM's three phases are monitored at the common coupling point. \( V_{grid} \)

As illustrated in equations (18) and (19), the error signal is routed to a second PI controller, whose outputs are transformed to reference currents in the q-axis i.q. The Clark and Park transformation matrices are necessary to convert the three-phase system to a d-q system and then evaluate the control signal's reference currents value. The three phase load currents are used as a reference signal to determine the actual current values in the d-q axis (i (d^*), q^*) using sensors. In the equation, the transformation matrix is described (20). A DC Link voltage signal sensor which fixed on the capacitor link of VSC is used to provide the voltage signal of DC \( V_{dc} \). The error signal evaluated from subtracting the \( V_{dc} \) from the DC reference voltage \( V_{dc,rref} \) is transformed to current signal (losses of switches VSC) using first PI controller as defined in the equations (21) and (22). In the last stage, the real current \( i_q \) adds to \( (i_{qdc}) \) which form the reference current \( (i^*_q) \) while the actual current \( (i_{qdc}) \) adds to losses current \( (i_{loss}) \) which produce the reference current \( (i^*_q) \) as explain in Fig. 6 [32]. Then an inverse transformation is applied on reference
currents $i_{d,ref}$ and $i_{q,ref}$ to compute the three phase reference currents ($i_{abc,ref}$). The resulted three phase currents are compared with source currents that contains an injected load currents harmonics where the generated signals used to produce appropriate switching signals for turn ON/OFF the gates of IGBTs switches.

(18) \[ i_q(n) = i_q(n-1) + K_{pi} (V_{acE(n)} - V_{acE(n-1)}) + K_{iq} v_{acE(n)} \]

(19) \[ i_q^* = i_{dqdc} + i_{ip} \]

(20) \[ \begin{bmatrix} i_d^* \\ i_q^* \\ i_0 \end{bmatrix} = \begin{bmatrix} \sin \theta & \sin(\theta - \frac{2\pi}{3}) & \sin(\theta + \frac{2\pi}{3}) \\ \cos \theta & \cos(\theta - \frac{2\pi}{3}) & \cos(\theta + \frac{2\pi}{3}) \end{bmatrix} \begin{bmatrix} \frac{1}{6} \\ \frac{1}{6} \\ \frac{1}{6} \end{bmatrix} \]

(21) \[ i_d^* = i_{dqdc} + i_{iust} \]

Where: \( V_{acE(n)} \) is the error signal generated by compare the reference \( V_{acE}^* \) with sensed grid voltage signal. \( V_{acE(n)} \) is the signal of error resulted from subtracting the reference and sensed signals of DC link voltage for nth sample. \( K_{ip}, K_{pdc} \) are represent gain factors of integral and proportional of first PI controller of DC link voltage. \( K_{iq}, K_{pqt} \) are the gain factors of integral and proportional of the second PI controller at PCC.

Fig.6. Schematic diagram of DSTATCOM control circuit using SRF.

Results and Discussion
A PV system configuration is connected across the DC link input voltage of DSTATCOM using MATLAB SIMULINK 2018a to simulate the DSTATECOM compensator with control circuits. A three-phase four-wire grid with a synchronous generator of kVA provides power to the non-linear demand in the considered power system. Table 2 lists the values of all components used in the current design.

<table>
<thead>
<tr>
<th>Parameters of the design work</th>
<th>Datum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Frequency and Three-phase voltage of Grid</td>
<td>50Hz, 415V</td>
</tr>
<tr>
<td>Impedance of Line Per Phase (Rs, Ls)</td>
<td>0.01Ω, 2 mH</td>
</tr>
<tr>
<td>Nonlinear Load (Three phase bridge)</td>
<td>130 Ω, 10mH, rectifiers with RL)</td>
</tr>
<tr>
<td>Ripple filter (Rf, Cf).</td>
<td>5 Ω, 5μf</td>
</tr>
<tr>
<td>Ac inductor</td>
<td>3.3 mH</td>
</tr>
<tr>
<td>DSTATCOM capacitor of Dc-link</td>
<td>2900 μf</td>
</tr>
<tr>
<td>DSTATCOM voltage of Dc-link</td>
<td>680 V</td>
</tr>
<tr>
<td>DC PI.1 ( (K_{ip}, K_{pdc}) )</td>
<td>0.2, 1</td>
</tr>
<tr>
<td>PCC voltage PI.2 ( (K_{iq}, K_{pqt}) )</td>
<td>0.01, 2</td>
</tr>
<tr>
<td>PI controller of logical coordination circuit (KP, KI)</td>
<td>0.8 \times 10^{-5}, 0.8 \times 10^{-8}</td>
</tr>
<tr>
<td>Current and Voltage of Battery (Vb, Ib)</td>
<td>550V, 13Ah</td>
</tr>
</tbody>
</table>

Fig 7. Three-phase synchronous generator supply a non-linear load without DSTATCOM compensation Voltage waveform.

In Fig 8 the three phase current waveform without compensation is present.

Fig 8. Three-phase current source before compensation.

Fig 9 shows the THD% for three phase current of source before the compensation.

Fig 9. The THD% of three phase current source before compensation.

When the STATCOM is powered by a battery, Fig. 10 displays the time response of the compensation effect for dc-link voltage. The settling time is 0.07s, and the influence of cut-off wires on the VSC dc-link voltage for DSTATCOM is absent.

Fig 10. DC link channel voltage time response when the DSTATCOM is fed from the PV system energy sources system. Table 3 shows the response times for all potential energy sources for DSTATCOM's dc-channel. The PV system system, when compared to the other sources, clearly provides a greater response. When employing Arduino sensors and the value of current source 31A, Fig11 displays the three phase current of the source after compensation.
are improved when compared to the performance of a constant DC voltage supply.

Authors
Ayman Dhafer abdul-nafa
I have graduated from Medical Instrumentation Technology Engineering Department Of Technical Engineering College Of Mosul at Northern Technical University. I get a master degree of technology in Medical instrumentation Technique Engineering of Electrical Engineering Technical College at Middle Technical University. I am working as assistant lecture in college of dentistry in university of Mosul2021. ayman@juomosul.edu.iq

Suhasa beeh ahmed
I have graduated from Medical instrumentation Technique Engineering of Electrical Engineering Technical College at Middle Technical University at 2006. I get a master degree of technology in Medical instrumentation Technique Engineering of Electrical Engineering Technical College at Middle Technical University. I am working as assistant lecture in Electrical Engineering Technical College at Middle Technical University 2021. suhasabeh@mtu.edu.iq

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