Improving performance of non-interior point based optimal power flow algorithm computations

Abstract. This paper presents the non-interior point method (NIP) based optimal power flow (OPF) algorithm parallelization and performance improvement experiments. The aim is to investigate the impact of parallelization techniques on overall OPF computations speedup. Presented approach takes advantage of the structure of algorithm and exploits it with the usage of multithreading to gain computation speedup. Obtained results give insight into the impact of multithreading techniques and algorithm initialization techniques.

Streszczenie. W artykule zaprezentowano wyniki eksperymentów prowadzących do redukcji czasu realizacji obliczeń algorytmu metody non-interior point (NIP) w zastosowaniu do zadania optymalizacji rozsądu mocy (OPF). Celem pracy było zbadanie wpływu zastosowania technik zrównoleglenia obliczeń na czas realizacji zadania OPF. W zaprezentowanym podejściu brano pod uwagę strukturę algorytmu oraz wykorzystano implementację wielowątkową. Uzyskane wyniki pokazują wpływ wielowątkowej implementacji oraz zastosowanych technik inicjalizacji algorytmu na czas obliczeń. (Poprawa wydajności algorytmu metody non-interior point w zastosowaniu do zadania optymalizacji rozsądu mocy).

Keywords: non-interior point method, optimal power flow, parallel computing.

Słowa kluczowe: metoda non-interior point, optymalizacja rozsądu mocy, obliczenia równoległe.

Introduction

Optimal power flow (OPF), in general, is related to power system operational and planning optimization methods [1]. The main idea behind OPF is to determine the optimal values for control variables while respecting various constraints. Over the years, a number of methods have been applied to solve the OPF problem [2], using various optimization techniques, but their effectiveness and performance is highly dependent on the size of a power system being optimized [3]. The development of the OPF recently has tracked significant progress both in numerical optimization techniques and computer techniques application. In recent years, application of interior point (IP) and non-interior point (NIP) methods to solve OPF problem has been paid great attention [4], [5]. This is due to the fact that IP method based algorithms are among the fastest algorithms, well suited to solve large-scale nonlinear optimization problems [5].

And yet, it’s still a great striving to improve those methods, especially in comparison with the current trends in the development of parallel programming techniques. The vast availability of multi-core CPUs, and multi-threaded programming libraries, inspires the search for solutions affecting not only the computational performance, but also application responsiveness.

Optimal power flow formulation

In general, the OPF problem may be defined mathematically as the nonlinear programming problem (NLP), with equality and inequality constraints, which is written as follows:

\[
\begin{align*}
\min_{x} & \quad f(x), \\
\text{subject to} & \quad h(x) = 0, \\
& \quad g(x) \leq 0,
\end{align*}
\]

(1)

where: \(x\) – the decision variables, \(f(x)\) – objective function, \(h(x)\) – vector of equality constraints, \(g(x)\) – vector of inequality constraints.

In the OPF formulation, the decision variables are represented by voltage magnitudes and angles, active and reactive power generation values. The objective function \(f(x)\) may represent total cost losses, total MW generation, transmission losses, etc. The equality constraints \(h(x)\) represents the power flow equations and \(g(x)\) includes functions corresponding to the inequality constraints and variables limits, such as bus voltage limits, active and reactive power generation limits, lines power flow limits.

Non-interior point method

One way to solve the problem (1), with respect to the interior-point method formulation [6], is to transform the inequality constraints into equality constraints by incorporating a barrier function and the non-negative slack variables vector \(z\),

\[
\begin{align*}
\min_{x} & \quad f(x) - \sum_{i=1}^{n_g} \mu_i \ln(z_i), \\
\text{subject to} & \quad h(x) = 0, \\
& \quad g(x) + z = 0,
\end{align*}
\]

(2)

where: \(n_g\) – number of inequality constraints, \(\mu_i\) – barrier parameter, \(z\) – vector of slack variables.

According to logarithmic barrier function, as the parameter \(\mu_i\) approaches to zero (in \(k\)-th iteration), the solution of problem (2) approaches to the solution of problem (1). The Karush–Kuhn–Tucker (KKT) optimality conditions for the (2) can be written as:

\[
\begin{align*}
Z\pi - \mu_i e + g(x) + z + \nabla_x f(x) + \nabla_x h(x) + \lambda + \nabla_x g(x) \pi = 0,
\end{align*}
\]

(3)

where: \(\nabla_x f(x)\) – partial derivative with respect to \(x\), \(e = [1, 1, \ldots, 1]^T\), \(Z = \text{diag}(z_i)\) for \(i = 1, ..., n_g\), \(\lambda\) and \(\pi\) – the vectors of Lagrange multipliers, corresponding to equality and inequality constraints.

The system of nonlinear equations (3) forms the basis of the calculation process of the interior point method. The equations (3) are solved using a variant of Newton’s method. The proper way of determining the value of barrier parameter \(\mu_i\), and controlling the Newton’s method step size in subsequent iterations, are amongst the most important elements affecting convergence of IP method.
The non-interior point method adopts the technique used in the class of complementarity problems [4]. It involves the introduction of so-called smoothing function (with additional parameter \( \mu \)):

\[ \varphi_{\mu}(z, \pi) = 0 \iff \pi > 0, \quad z > 0, \quad \pi z = \mu \quad \text{for} \quad \mu > 0, \]

in the place of complementarity conditions, in the system of equations resulting from KKT conditions. It leads to the following system of nonlinear equations:

\[ \psi(y) = \begin{bmatrix} r_n \cr r_m \cr \nabla_x f(x) + (\nabla_x h(x))^T \lambda + (\nabla_x g(x))^T \pi \end{bmatrix} = 0, \]

where: \( \Phi_{\mu}(z, \pi) = \{ \varphi_{\mu}(z_i, \pi_i) \}_{i=1,2 \ldots, n}, \mu = \mu e, \)
\( y = [z, \pi, x, \lambda, \mu]^T. \) \( e \in (0,1). \)

Furthermore, the smoothing parameter \( \mu \) (with scaling parameter \( \sigma \)) is treated as an additional variable, in the system of equations (5). In the experiments, the modified Fischer-Burmeister function of the following form has been used:

\[ \varphi_{\mu}(z, \pi) = z + \pi - \sqrt{z^2 + \pi^2 + 2 \mu}. \]

It is assumed that the system (5) is solved using damped Newton’s method, and in the subsequent iterations value of parameter \( \mu \) is reduced to zero. At the k-th iteration, the update \( \Delta \mu \) is determined by solving the following sparse system of linear equations:

\[ \nabla_{\mu} \psi(y^k) \Delta \mu = -\psi(y^k), \]

where: \( \nabla_{\mu} \psi_{\mu}(y^k) \) - Jacobi matrix of vector function \( \psi_{\mu}(y^k) \) at point \( y^k \), \( \Delta \mu = [\Delta z, \Delta \pi, \Delta x, \Delta \lambda, \Delta \mu]^T. \)

The solution of system of equations (7) corresponds to the solution of a linear system of equations, which in matrix notation takes the following form:

\[ \begin{bmatrix} D_z & D_\pi & 0 & 0 & D_x \\ 0 & 0 & \nabla_x g(x) & 0 & 0 \\ 0 & 0 & \nabla_x h(x) & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} \Delta z \\ \Delta \pi \\ \Delta x \\ \Delta \lambda \\ \Delta \mu \end{bmatrix} = -\psi(y^k), \]

where:
\( D_z = \nabla_{z} \Phi_{\mu} = \text{diag}(\partial_{z_i} \varphi_{\mu}(z_i, \pi_i) / \partial z_i; i = 1,2 \ldots, n), \)
\( D_{\pi} = \nabla_{\pi} \Phi_{\mu} = \text{diag}(\partial_{\pi_i} \varphi_{\mu}(z_i, \pi_i) / \partial \pi_i; i = 1,2 \ldots, n) \),
\( D_x = \nabla_{x} \Phi_{\mu} = \text{diag}(\partial_{x_i} \varphi_{\mu}(z_i, \pi_i) / \partial x_i; i = 1,2 \ldots, n), \)
\( V_{\nabla L}^2 = \nabla_{\nabla L} f(x) + \nabla_{\nabla L} (\lambda^T h(x)) + \nabla_{\nabla L} (\pi^T g(x)). \)

The new value of the vector of variables \( \Delta y \) is determined according to the equation:

\[ y^{k+1} = y^k + \alpha^k \Delta y^k, \]

where: \( k \) - number of iteration, \( \alpha^k \) - step length in the direction of vector \( \Delta y^k \).

The choice of step length \( \alpha^k \) is based on the backtracking Armijo line search rule, in which the distance from the solution point is determined by the equation:

\[ \theta^k(z, \pi) = \begin{cases} \Phi_{\mu}(z, \pi)^T \Phi_{\mu}(z, \pi) / 2 & \text{for} \quad \theta^k(z, \pi) > 0, \\ 0 & \text{for} \quad \theta^k(z, \pi) \leq 0, \end{cases} \]

The choice of step length \( \alpha^k \) is performed according to the following rule: for given values of coefficients, \( \alpha e(0,1) \) find such:

\[ \alpha^k = \max\{\alpha^k_0: p=0,1,2,\ldots\}, \]

for which the following condition is fulfilled:

\[ \theta^k(z^p, \pi^p) \leq \beta(1-\alpha^k)^2 \mu^k, \]

where \( \beta = \theta_{\mu}(z^0, \pi^0)^2 / \mu^0. \)

For step length \( \alpha^k \) determined in this manner, the new value of the variables vector \( y \) is determined, according to the equation (9).

**Analytical reduction of the system**

One of the improvements that has great impact on the NIP algorithm computation time [4] is the analytical reduction of the system of equations (8), which leads to the formulation of a reduced system:

\[ \begin{bmatrix} W & \nabla_x h(x)^T \\ \nabla_x h(x) & 0 \end{bmatrix} \begin{bmatrix} \Delta z \\ \Delta \pi \end{bmatrix} = \begin{bmatrix} s \\ \nabla_x h(x) \end{bmatrix}, \]

where:
\( W = V_{\nabla L}^2 + \nabla_x g(x)^T D_z \nabla_x g(x), \)
\( s = r_n + \nabla_x g(x)^T D_z (-r_n + \mathbf{D}_z \nabla_x h(x)) \).

Then, variables update \( \Delta z \) and \( \Delta \pi \) values are computed using the following equations:

\[ \Delta z = -r_n - \nabla_x g(x)^T \Delta z, \]
\[ \Delta \pi = \mathbf{D}_z (r_n - \mathbf{D}_z \Delta z - \mathbf{c}_\pi \mathbf{D}_\mu). \]

**Parallelization and further algorithm improvements**

Presented non-interior point algorithm variant is not directly dedicated (by design) to efficient parallel implementation. However, by thoroughly studying the structure and performance of the sequential variant of NIP algorithm, some improvements have been proposed.

The main idea behind the parallelization of the OPF computations using presented non-interior point algorithm implementation is to distribute the most time consuming, independent components computations across multiple computing units (processors).

Each iteration of NIP method involves the computation of vector of equality constraints \( h(x) \), vector of inequality constraints \( g(x) \), vector \( \nabla_x f(x) \) and matrices \( \nabla_x h(x) \) and \( \nabla_x g(x) \). These components are computed as independent tasks to form right-hand-side of (13). In order to obtain \( V_{\nabla L}^2 \) matrix, and hence \( W \) matrix, Hessian matrices: \( \nabla^2_x (\lambda^T h(x)) \) and \( \nabla^2_x (\pi^T g(x)) \) and \( \nabla^2_x f(x) \) are also computed as independent tasks. An important fact worth mentioning is, that in the implemented algorithm, individual components of the matrix (8) and hence (13) were determined analytically. In addition, each iteration also involves the variables update, which is also computed using multithreading.

Moreover, it is possible to obtain some further improvements that lead to the shortening the time of calculations, by the proper selection of algorithm start point.

Proposed, modified non-interior point based optimal power flow algorithm initialization technique is based on power flow solution—in contrast to the standard method used [4] (so called flat-start), in which initial values of angles were used.
set to zero, and the values of voltage, active and reactive power modules at production buses were set at the average calculated from the minimum and maximum permissible values.

As observed, performing the power flow (PF) calculations before the actual OPF algorithm execution, and using the PF solution values as initial values to the OPF algorithm, reduces the number of iterations needed to obtain the final result. Therefore, reducing the number of iterations needed to obtain the solution, results in the shortening overall execution time.

Both of the mentioned algorithm improvements have been implemented and tested computationally.

Computational experiments

The experiments have been performed using software created in C# language, with C/C++ wrapper library to the Intel® Math Kernel Library (MKL) routines.

The developed software utilizes a custom object-oriented C# implementation of sparse matrix representation for storing data used in the application. The algorithms, mentioned, have been implemented in the C# language also. MKL library [7] has been used to solve sparse linear systems via LU factorization with sparse matrix minimum degree ordering.

In multithreaded variant of algorithm implementation the Task Parallel Library (TPL) [8], [9] has been used. The main advantage of using TPL is the fact, that it scales the degree of concurrency dynamically to most efficiently use all the processors that are available in the system. In addition, it handles the partitioning of the work, the scheduling of threads on the ThreadPool, cancellation support and state management.

Test systems data are derived from the MATPOWER package available for scientific and educational applications [10]. The 300, 2383 and 2746 bus test systems, were used in the simulations. Some statistics for the test power systems are presented in Table 1, where \( N_b \) denotes the number of buses, \( N_g \) denotes number of generator buses, \( N_o \) symbolizes the number of load buses and \( N_l \) represents the number of branches.

<table>
<thead>
<tr>
<th>Test system</th>
<th>( N_b )</th>
<th>( N_g )</th>
<th>( N_o )</th>
<th>( N_l )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case-300</td>
<td>300</td>
<td>69</td>
<td>231</td>
<td>411</td>
</tr>
<tr>
<td>Case-2383</td>
<td>2383</td>
<td>327</td>
<td>2056</td>
<td>2696</td>
</tr>
<tr>
<td>Case-2746</td>
<td>2746</td>
<td>363</td>
<td>2383</td>
<td>3279</td>
</tr>
</tbody>
</table>

Some statistics for the non-interior-point based non-linear programming problem (NLP) are displayed in Table 2, where, for each test system, is given: the number of decision variables \( n_x \), the number of equality constraints \( n_e \), the number of inequality constraints \( n_p \), square matrix (13) size of the analytically reduced system, denoted by \( M \).

<table>
<thead>
<tr>
<th>Test system</th>
<th>( n_x )</th>
<th>( n_e )</th>
<th>( n_p )</th>
<th>( M )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case-300</td>
<td>738</td>
<td>600</td>
<td>1698</td>
<td>1338</td>
</tr>
<tr>
<td>Case-2383</td>
<td>5420</td>
<td>4766</td>
<td>11866</td>
<td>10186</td>
</tr>
<tr>
<td>Case-2746</td>
<td>6220</td>
<td>5492</td>
<td>13506</td>
<td>11712</td>
</tr>
</tbody>
</table>

To compare the performance of the implemented sequential NIP based OPF method algorithm, the multi-threaded (MT) version of the algorithm has been implemented utilizing .NET 4 Task Parallel Library (TPL) [8], [9]. The results of experiments are shown in Table 3, where \( T_{SEQ} \) denotes sequential algorithm execution time, \( T_{MT} \) denotes multi-threaded algorithm execution time and finally \( T_{MTPF} \) denotes multi-threaded algorithm execution time with power flow initialization technique applied. \( S_M \) and \( S_{MPF} \) denote speedup of algorithms, respectively multi-threaded version and multi-threaded version with power flow initialization technique.

All of the experiments were performed utilizing implementation of non-interior point algorithm variant, with analytically reduced system of linear equations (13).

Computations were performed using the Intel Core i7-3770K @ 3.5 GHz machine with the 64-bit edition of the Microsoft Windows 10 operating system.

<table>
<thead>
<tr>
<th>Test system</th>
<th>( T_{SEQ} ) (ms)</th>
<th>( T_{MT} ) (ms)</th>
<th>( S_M = \frac{T_{SEQ}}{T_{MT}} )</th>
<th>( T_{MTPF} ) (ms)</th>
<th>( S_{MPF} = \frac{T_{SEQ}}{T_{MTPF}} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Case-300</td>
<td>409</td>
<td>377</td>
<td>1,09</td>
<td>351</td>
<td>1,17</td>
</tr>
<tr>
<td>Case-2383</td>
<td>5191</td>
<td>4373</td>
<td>1,19</td>
<td>4098</td>
<td>1,27</td>
</tr>
<tr>
<td>Case-2746</td>
<td>5877</td>
<td>4639</td>
<td>1,27</td>
<td>4382</td>
<td>1,34</td>
</tr>
</tbody>
</table>

Figure 1 illustrates the overall optimal power flow computation time in milliseconds depending on the test system case. Growing value of speedup for larger system cases (figure 2) indicates future promise for using such algorithm improvements in order to obtain substantial performance benefits for large problems.

Figure 2. Multithreaded variant of NIP based OPF speedup

Table 4 collects the results of experiments, that shows the influence of proposed initialization technique to the number of iterations of OPF algorithm and therefore to execution time.
Table 4. NIP method based optimal power flow results obtained for multithreaded variant of algorithm with PF initialization technique

<table>
<thead>
<tr>
<th>Test system</th>
<th>MT with flat start</th>
<th>MT with PF initialization technique</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>No. of iter.</td>
<td>T_MT</td>
</tr>
<tr>
<td></td>
<td>ms</td>
<td></td>
</tr>
<tr>
<td>Case-300</td>
<td>17</td>
<td>377</td>
</tr>
<tr>
<td>Case-2383</td>
<td>25</td>
<td>4373</td>
</tr>
<tr>
<td>Case-2746</td>
<td>24</td>
<td>4639</td>
</tr>
</tbody>
</table>

In table 4, MT denotes multithreaded variant of implemented algorithm, T_PF denotes power flow algorithm execution time. Additionally, for a specific test system, in a series of experiments, algorithm converged to the same solution point (with respect to the value of the objective function and variables vector values) meeting given calculation accuracy.

**Conclusion**

This paper discusses some issues that are directly related to an efficient implementation of the non-interior point algorithm for nonlinear formulation of optimal power flow problem.

Although, the nonlinear NIP algorithm is not well-suited to parallel implementations, it is worth to utilize the possibilities given by multithreaded architectures and compute independently some components during algorithm execution. Presented approach takes advantage of the problem structure in order to obtain increased efficiency for the overall method. The block structure of the linear system matrix solved in each iteration of the NIP method has been exploited in the parallel computation.

Task Parallel Library significantly simplifies parallel development by providing routines that can automatically distribute tasks across the computer's available CPUs and allows to create parallel code in a natural way without having to work directly with threads or the thread pool.

Accordingly to further improvements. The sparsity pattern of individual components of the matrix (13) as well as reordering to minimize fill-in remain the same. Since, the computation of linear system of equations within an iteration of NIP algorithm, demands great computational effort, it may be advantageous to use the same, given matrix sparsity pattern in subsequent iterations.

**REFERENCES**